

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Assaf Shappir	Examiner:	Nguyen, Dang T
Serial No.:	10/537,857	Group Art Unit:	2824
Filed:	06/07/2005		
Title:	Method, circuit and system for erasing one or more non-volatile memory cells		

AMENDMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is filed in response to the Office Action dated March 23rd, 2007 issued by the United States Patent and Trademark Office in connection with the above-identified Application. A response to the March 23rd, 2007 Office Action is due June 23rd, 2007. Accordingly, this communication is being timely filed.

Kindly amend the above-identified application and consider the remarks as follow:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.